

Trainings Processors (generic): This set of courses is not dedicated to a particular processor but describes generic concepts



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ACSYS has designed these generic processor courses to satisfy the following customer needs:

- Engineers working at the application level, who want to understand the processor architecture in order to optimize their application
- Engineers willing to acquire the knowledge of a processor family without detailing a particular processor core
- Engineers involved in low level user privilege programming

You can see detailed course descriptions of the various trainings by using the above navigation bar. You can also click on course identifiers in the following course briefs hereafter.

PG1 - Multicore implementation This course tackles multicore implementation issues

PG2 - PowerPC System Design This course has been designed for developers involved in a PowerPC development who want to understand generic mechanisms

PG3 - ColdFire System Design This course has been designed for developers involved in a ColdFire development who want to understand generic mechanisms