



Programmable Logic						
Course	Duration	Cost	2024			
			May	June	July	August
oRV1 - RISC-V Architecture	18 hours	2620 € HT				<i>on request</i>
oV1 - VHDL Language basics	24 hours	2740 € HT				<i>on request</i>
oV2 - Advanced VHDL for FPGA	18 hours	2510 € HT				<i>on request</i>