Trainings Logique Programmable: Programmation et mise en oeuvre d'électronique programmable - Programming

Logique Programmable

Programmation et mise en oeuvre d'électronique programmable

La réalisation de systèmes embarqués combine de plus en plus de l'électronique programmable et du logiciel. Ces deux composantes du système contribuent de façon critique au bon fonctionnement du système et doivent être conçues et maîtrisées non seulement séparément mais également dans leurs interactions. **ac6-formation** propose des formations pratiques pour vous permettre de maîtriser la mise en oeuvre de composants de logique programmable et leur interactions avec la composante logicielle de vos systèmes.

You can see detailed course descriptions of the various trainings by using the above navigation bar. You can also click on course identifiers in the following course briefs hereafter.

ALT1 - CYCLONE-V CORTEX-A9 HARD PROCESSOR SYSTEM This course covers the hard IPs present in Cyclone-V Intel (Altera) FPGA family, based on ARM Cortex-A9 CPU
H1 - Lattice Mico32 FPGA embedded processor Implementing and programming a processor core in an FPGA
H2 - Lattice Diamond Mastering Diamond for FPGA optimisation and debug
HX4 - Xilinx - Microblaze implementation This course explains how to design a SoC based on MicroBlaze, Xilinx proprietary IPs and/or custom IPs using EDK
HX5 - Zynq All Programmable SoC: Hardware and Software Design This course explains how to design a System on Chip (SoC) based on the Zynq-7000 All Programmable SoC
MSP - Microsemi SmartFusion2 Programming This course describe the Microcontroller Subsystem (MSS) of SmartFusion2 Microsemi Microchip FPGAs
RV1 - RISC-V CPU This course covers and explains the implementation of the RISC-V CPU on FPGA platforms
V0 - Programmable component fundamentals This training is intended to professional who want to use or maintain programmable components
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V1 - VHDL Language FPGA Programming with VHDL and Simulation (through the training Xilinx, Lattice or Actel FPGA are targeted)
V2 - Advanced VHDL for FPGA Acquire a strong design methodology with the best of VHDL for simulation and synthesis
V3 - Design with SystemC System Design and Simulation with SystemC