

Courses on NXP i.MX SoCs

ACSYS offers a large set of courses on NXP processors.

Each course details both hardware and software implementation of these processors.

Examples are provided to explain low level programming, which is needed to understand the boot program.

For on-site trainings, an additional day covering Linux porting or Windows Embedded porting may be appended to i.MX processor courses.

Cours principaux

FA4 - i.MX6 Implementation This course describes the i.MX6 Dual and Quad core SoC

FA5 - i.MX8m Implementation This course describes the i.MX8m Dual and Quad core SoC

FA6 - i.MX8 Max Implementation This course describes the i.MX8 Quad Plus and Quad Max core SoC Note this course can only be provided to NXP-approved customers, the SoC Reference Manual being still only available under a Non-Disclosure Agreement. Contact us at training@ac6-training.com for any further details.

FK1 - Kinetis MCU Implementation This course covers all NXP MCUs belonging to the Kinetis families K10, K20, K30, K40 and K60

FK2 - Kinetis KL26z MCU Implementation This course covers the NXP Kinetis KL26z ultra low power MCU

FQ1 - LS1021A QorIQ implementation This course covers the LayerScape LS1021A SoC

NP1 - LPC21XX/LPC22XX microcontroller implementation This course covers NXP ARM-based MCU family

NP2 - LPC17xx microcontroller implementation This course covers NXP Cortex-M3-based LPC17XX MCU family.

Autres cours

RT3 - Programmation temps réel avec FreeRTOS La programmation temps réel appliquée au système d'exploitation FreeRTOS