.calendar { width: 100%; border-collapse: collapse; } .calendar th, .calendar td { border: 1px solid #ddd; padding: 8px; } .calendar th { background-color: #f2f2f2; text-align: center; } .calendar tr:nth-child(even) { background-color: #f9f9f9; } .calendar tr:hover { background-color: #ddd; } .calendar .cal_header { background-color: #4CAF50; color: white; } .calendar .cal_category { background-color: #2196F3; color: white; } .calendar .cal_col_header { background-color: #f2f2f2; } .calendar .cal_c_even { background-color: #ffffff; } .calendar .cal_c_odd { background-color: #f9f9f9; } .calendar .cal_c_even_s_even, .calendar .cal_c_even_s_odd, .calendar .cal_c_odd_s_even, .calendar .cal_c_odd_s_odd { background-color: #ffffff; } .calendar a { color: #2196F3; text-decoration: none; } .calendar a:hover { text-decoration: underline; }

FPGA					
Course	Duration	2025			
		May	June	July	August
ALT1 - CYCLONE-V CORTEX-A9 HARD PROCESSOR SYSTEM	5 days	*on request*			
H1 - Lattice Mico32 FPGA embedded processor	3 days	*on request*			
H2 - Lattice Diamond	2 days	*on request*			
HX4 - AMD (Xilinx) - Microblaze implementation	2 days	*on request*			
HX5 - AMD Zynq All Programmable SoC: Hardware and Software Design	2 days	*on request*			
MSP - Microchip SmartFusion2 Programming	3 days	*on request*			
RV1 - RISC-V Architecture	3 days	*on request*			
V0 - Programmable components fundamentals	2 days	*on request*			
V1 - VHDL Language Basics	4 days	*on request*			
V2 - Advanced VHDL for FPGA	3 days	*on request*			
V3 - Design with SystemC	4 days	*on request*			