IM4 - SlimBus

This course covers the Serial Low-Power Inter-Chip Media Bus (SLIMBUS)

Objectives

- The course starts with an overview of MIPI specification.
- The physical layer is explained as well as all conformance tests.
- Protocol is then described, clarifying how a frame can be routed in a point-to-multipoint system through TDM.
- Message synchronization and tracking mechanisms are also covered.
- The course also focuses on the enumeration sequence and the gear change sequence.
- Companies interested in attending this course must adhere to MIPI organization.
- This course has been designed for engineers in charge of SoC architecture, functional verification or silicon validation.

A more detailed course description is available on request at training@ac6-training.com

Prerequisites

- Basic knowledge on digital electronics

Course Environment

- Theoretical course
  - PDF material in English (printed for face-to-face); online over Teams.
  - Trainer assistance throughout.
- Each session starts with a trainee check-in.

Target Audience

- Any embedded systems engineer or technician with the above prerequisites.

Course Outline

Introduction to mipi specifications

Serial low-power inter-chip media bus (slimbus)

- Overview
- SLIMbus model
- Physical layer
- Frame structure
- Information elements and value elements
- Message protocol and tracking
- Transport protocols
- Bus processes
- Device classes
- Test suites