



```
.calendar { width: 100%; border-collapse: collapse; } .calendar th, .calendar td { border: 1px solid #ddd; padding: 8px; } .calendar th { background-color: #f2f2f2; text-align: center; } .calendar tr:nth-child(even) { background-color: #f9f9f9; } .calendar tr:hover { background-color: #ddd; } .calendar .cal_header { background-color: #4CAF50; color: white; } .calendar .cal_category { background-color: #2196F3; color: white; } .calendar .cal_col_header { background-color: #f2f2f2; } .calendar .cal_c_even { background-color: #ffffff; } .calendar .cal_c_odd { background-color: #f9f9f9; } .calendar .cal_c_even_s_even, .calendar .cal_c_even_s_odd, .calendar .cal_c_odd_s_even, .calendar .cal_c_odd_s_odd { background-color: #ffffff; } .calendar a { color: #2196F3; text-decoration: none; } .calendar a:hover { text-decoration: underline; }
```

FPGA		
Cours	Durée	2025
		septembre
oRV1 - Architecture RISC-V	18 heures	sur demande
oV1 - Les bases du langage VHDL	24 heures	08-11- Online EurAsia
oV2 - VHDL avancé pour les FPGA	18 heures	sur demande