Processors

Les processeurs ARM sont les plus utilisés dans les systèmes embarqués mais ne sont pas les seuls. Ac6 propose des cours sur la plupart des architectures, décrivant les différents cœurs et SoCs (System on Chip) basés sur ces cœurs.

Vous pouvez visualiser la liste des cours de chaque catégorie en utilisant le carrousel ci dessus. Vous pouvez également cliquer sur les catégories de cours dans les descriptions ci dessous.

ARM Cores - Courses on ARM cores ACSYS offers a large set of courses on ARM processor cores.
Each course details both hardware and software implementation of these cores.
Programming examples are provided to clarify the operation of complex assembly instructions and to explain the parameterizing of the ARM linker.

NXP ARM SoCs - Courses on NXP i.MX SoCs ACSYS offers a large set of courses on NXP processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming, which is needed to understand the boot program.
For on-site trainings, an additional day covering Linux porting or Windows Embedded porting may be appended to i.MX processor courses.

ST processors - Courses on ST processors based on ARM cores ACSYS offers a large set of courses on ST processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming and particularly how to use the software package provided by ST.

TI processors - Courses on TI SoCs based on ARM cores ACSYS offers a large set of courses on TI processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming, which is needed to understand the boot program.
The course also contains an introduction to Code Composer Studio IDE.

NXP Power CPUs - Courses on NXP Power processors ACSYS offers a large set of courses on NXP processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming, which is needed to understand the boot program.
For on-site trainings, an additional day on Linux porting or Windows Embedded porting may be appended to the processor course.